

Amendments to the Specification:

Please add the following paragraph [0018.1] after paragraph [0018].

[0018.1] Figure 7A is a schematic elevation view of the a processor pin interfacing with a receptacle on the socket;

Please replace paragraphs [0025], [0028], and [0030] with the following amended paragraphs:

[0025] Figure 3 and Figure 4 depict, respectively, schematic representations of the interfacing portions of heat sink assembly 10 with a processor 26 installed and the corresponding interfacing portions of circuit board 30 and chip socket 34. Circuit board 30 has a plurality of holes 32 that are sized and spaced to accommodate alignment pins 14. Alignment pins 14 entering holes 32 serves as the initial gross alignment of the processor chip 26 and the socket 34. As the heat sink assembly 10 is lowered, locating tabs 18 interface with corresponding slots 36 integrally constructed into socket 34 and tabs 16 are spaced so as to fit along edges 38, 40 of socket 34. The interaction of locating tabs 16,18 and the socket 34 ensures that processor pins 28 will enter the corresponding receptacles 42 built into socket 34. In addition, clips 20 have clearance slots 35 built into socket 34. Once processor 26 is fully engaged into socket 34, locking lever 44 is actuated, which locks the processor in place and couples the processor to the circuit board. Locking lever 44 located at the end of an extended arm ~~[[46]]~~ 47 so that it can be accessed when the heat sink assembly 10 is installed.

[0028] Figure 7 ~~and Figure 7A show~~ shows the assembly 10 as processor pins 28 are aligning with mounting holes 42. Each mounting hole 42 has a chamfered entrance 43 that combined with the chamfered, or pointed, end 29 of pin 28 allows the pin to smoothly enter the mounting hole.

[0030] Figure 9 shows an end view of socket 34 with locking lever 44 shown in both an open position 46 and a closed position 48 (shown in phantom lines). In both open 46 and closed 48 positions, locking lever 44 is elevated above, and at an angle to, board 30. This provides clearance beneath locking lever 44 for other components to be mounted to board 30 and makes it easier for a user to manually actuate the lever. Referring back to Figure [[1]] 2, cage 12 preferably comprises a tab 52 located at one corner of the cage. The closed position 48 of locking lever 44 is arranged so as to interfere with tab 52 and prevent insertion of processor 26 into socket 34 if it is closed.